

Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 249979US2S	SERIAL NO. 10/792,324
LIST OF REFERENCES CITED BY APPLICANT <i>MAY 12 2004</i> <i>JCL</i>		APPLICANT Yuui SHIMIZU			
		FILING DATE March 4, 2004		GROUP 2827	
		U.S. PATENT DOCUMENTS			
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
Hf	AA 6,081,445	06/27/2000	J. SHI, et al.	/	/
	AB				
	AC				
	AD				
	AE				
	AF				
	AG				
	AH				
	AI				
	AJ				
	AK				
	AL				
	AM				
	AN				
FOREIGN PATENT DOCUMENTS					
	DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES NO	
Hf	AO 2003-242771	08/29/2003	JAPAN		X
	AP				
	AQ				
	AR				
	AS				
	AT				
	AU				
	AV				
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)					
Hf	AW	R. SCHEUERLEIN, et al., IEEE International Solid-State Circuits Conference, Digest paper, pages 128-129, "TA 7.2 A 10NS READ AND WRITE NON-VOLATILE MEMORY ARRAY USING A MAGNETIC TUNNEL JUNCTION AND FET SWITCH IN EACH CELL", 2000			
Hf	AX	M. DURLAM, et al., IEEE International Solid-State Circuits Conference, Digest Paper, pages 130-131, "TA 7.3 NONVOLATILE RAM BASED ON MAGNETIC TUNNEL JUNCTION ELEMENTS", 2000			
	AY				
	AZ	<input type="checkbox"/> Additional References sheet(s) attached			
Examiner <i>Huan Hoang</i>				Date Considered <i>6/20/05</i>	
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.					